Sheet 1 of 1 PATER PTO-1449 (SUBSTITUTE) Attorney Docket No.: Applic. No. GR 98 P 2661 P 09/816,923 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE **Applicant** Manfred Engelhardt INFORMATION DISCLOSURE STATEMENT BY APPLICANT Filing Date Group Art Unit (37 CFR 1.98(b)) 2825 2976 March 23, 2001 **U.S. PATENT DOCUMENTS** SUB FILING EXAMINER **INITIALS** PATENT NO. DATE **PATENTEE CLASS** CLASS DATE Α В C D E F G Н I FOREIGN PATENT DOCUMENT

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	0	"A High Performance 3.97µm ² CMOS SRAM Technology Using Self-Aligned			
Dz.	L I	Local Interconnect and Copper Interconnect Metallization" (Woo et al.), 1998			
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Az]	Thresholds and Copper Interconnects" (Su et al.), 1998 Symposium on VLSI			
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